PAT-NO: JP403175065A

DOCUMENT-IDENTIFIER: JP 03175065 A

TITLE: DATA BUFFER DEVICE

PUBN-DATE: July 30, 1991

INVENTOR-INFORMATION: NAME SATO, MAKOTO ATSUIZUMI, TAKAHIRO

ASSIGNEE-INFORMATION:

NAME COUNTRY CANON INC N/A

APPL-NO: JP01315066

APPL-DATE: December 4, 1989

INT-CL (IPC): B41J005/30, B41J029/38 , G06F003/12
US-CL-CURRENT: 235/432, 358/1.15 , 400/54 , 400/76

ABSTRACT:

PURPOSE: To easily interrupt/restart a receiving operation and to improve convenience by providing reception interrupt means for interrupting receiving operation of data to be transmitted from a data buffer device to a data sender, means for displaying an interrupt state, and reception restarting means for restarting the receiving operation.

CONSTITUTION: When a reception interrupt button 7 is pressed, a lighting reception display lamp 13 is flashed to inform a user of that the reception is

interrupted, and stood by until pressing of any of buttons for instructing an operation. If the button 7 is pressed in an operation instruction standby state at the time of interrupt, it is dealt with restart of a reception (release of the interrupt), the lamp 13 is lit to inform the user of the reception, and reception of data block of a block index (i) is continued. If a reception start/end button 6 is pressed in the operation instruction standby state at the time of interrupt, the data block received up to present is reqistered.

COPYRIGHT: (C)1991, JPO&Japio